



Substitute for form 1449A/B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/612,194-Conf. #4126
				Filing Date	July 3, 2003
				First Named Inventor	Chandra Mouli
				Art Unit	2826
				Examiner Name	T. N. Tran
Sheet	1	of	1	Attorney Docket Number	M4065.0933/P933

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TT	CA	Peters, Laura – "Choices and Challenges for Shallow Trench Isolation," ASML, Semiconductor International, April 1, 1999, 5 pages.	

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Examiner Signature		Date Considered	11/04
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PTO/SB/08a/b (08-03)
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TT	AA	2002/0021121 A1	02/21/2002	Nakamura	
TT	AB	2002/0054389 A1	05/09/2002	Takada et al.	

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TT	CA	S. G. Chamberlain et al. – "A Novel Wide Dynamic Range Silicon Photodetector and Linear Imaging Array," IEEE Journal of Solid-State Circuits, Vol. SC-19, No. 1, February 1984.			
TT	CB	A. J. Blanksby – "Performance Analysis of a Color CMOS Photogate Image Sensor," IEEE Transactions on Electron Device, Vol. 47, No. 1, January 2000.			

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